

FORM PTO-1449 (SUBSTITUTE)
U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE
STATEMENT BY APPLICANT
(37 CFR 1.98(b))

Attorney Docket No.:
GR 98 P 2078

RCE of Applic. No.
09/124,288

Applicant
Jens Barrenscheen et al.

RCE Filing Date
December 11, 2003

Group Art Unit
2455 2145

#23

U.S. PATENT DOCUMENTS

EXAMINER INITIALS		PATENT NO.	DATE	PATENTEE	CLASS	SUB CLASS	FILING DATE
<i>plw</i>	A	4,158,235	06/79	Call et al.			
	B						
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		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUB CLASS	TRANSL. YES/NO
	J						
	K						
	L						
	M						
	N						

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>plw</i>	O	"Software-Controlled Wrap-Around Buffer", IBM Technical Disclosure Bulletin, Vol. 28, No. 12, May 1986, pp. 5314-16.
<i>plw</i>	P	Tam-Anh Chu et al.: "Design of VLSI Asynchronous FIFO Queues for Packet Communication Netowrks", XP-000756967, IEEE, 1986, pp. 397-400.

EXAMINER

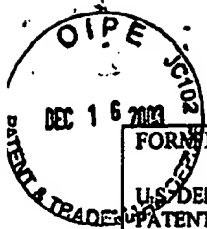
Patrice Winder

DATE CONSIDERED

October 15, 2004

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)

<i>plw</i>	O	S.G. Tzafestas: "On the Design of Multi-Input Periodic-Block-Output Buffers with Binomial Feedback", XP-002123624, pp. 120-125.
	P	

EXAMINER <i>Patrice Winder</i>	DATE CONSIDERED <i>October 15, 2004</i>
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